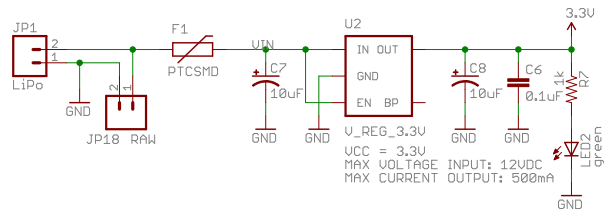
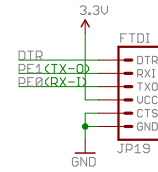


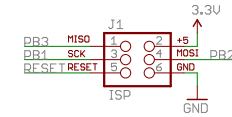
POWER



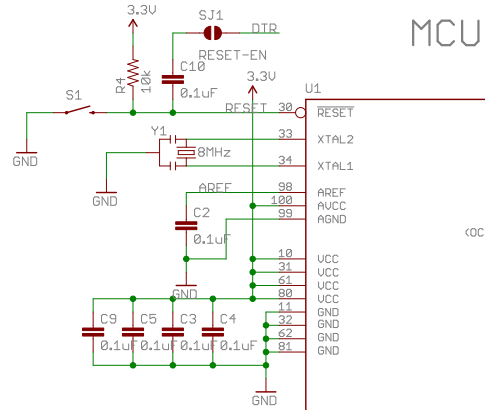
FTDI



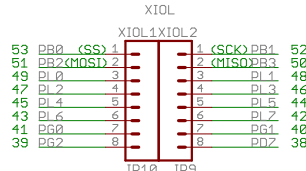
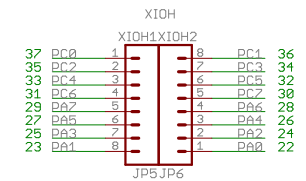
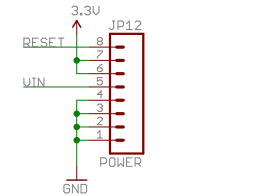
ISP



MCU



<AD7>PA7	71	DA7			
<AD6>PA6	72	DA6			
<AD5>PA5	73	DA5			
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<AD3>PA3	75	DA3			
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<OC1B/PCINT6>PB6	25	PB6	pwm	5	
<OC1A/PCINT5>PB5	24	PB5	pwm	4	
<OC2A/PCINT4>PB4	23	PB4	pwm	3	
<MISO/PCINT3>PB3	22	PB3 (MISO)	PH6	pwm	2
<MOSI/PCINT2>PB2	21	PB2 (MOSI)	PH5	pwm	1
<SCK/PCINT1>PB1	20	PB1 (SCK)			
<SS/PCINT0>PB0	19	PB0			
<A15>PC7	60	PC7			
<A14>PC6	59	PC6			
<A13>PC5	58	PC5			
<A12>PC4	57	PC4			
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<A10>PC2	55	PC2			
<A9>PC1	54	PC1			
<A8>PC0	53	PC0			
<T0>PD7	50	PD7	TXD3	8	14
<T1>PD6	49	PD6	RXD3	7	15
<XCK1>PD5	48	PD5	TXD2	6	16
<ICP1>PD4	47	PD4	RXD2	5	17
<TXD1/INT3>PD3	46	TXD1			18
<RXD1/INT2>PD2	45	RXD1			19
<SDA/INT1>PD1	44	SDA			20
<SCL/INT0>PD0	43	SCL			21
<CLKO/ICP3/INT7>PE7	9	PH4	pwm	8	7
<T3/INT6>PE6	8	PH3	pwm	7	6
<OC3C/INT5>PE5	7	PE5	pwm	6	5
<OC3B/INT4>PE4	6	PE4	pwm	5	4
<OC3A/AIN4>PE3	5	PE3	pwm	4	3
<XCK0/AIN0>PE2	4	PE2	pwm	3	2
<TXD0>PE1	3	PE1 (TX=0)PE1	pwm	2	1
<RXD0/PCIN8>PE0	2	PE0 (RX=1)PE0	pwm	1	0
<ADC7/TD1>PF7	90	ADC7			1
<ADC6/TD0>PF6	91	ADC6			2
<ADC5/TMS>PF5	92	ADC5			3
<ADC4/TCK>PF4	93	ADC4			4
<ADC3>PF3	94	ADC3			5
<ADC2>PF2	95	ADC2			6
<ADC1>PF1	96	ADC1			7
<ADC0>PF0	97	ADC0			8
<OC0B>PG5	1	pwm	PG5		
<TOSC1>PG4	28				
<TOSC2>PG3	29				
<ALE>PG2	70	PG2			
<RD>PG1	62	PG1			
<WR>PG0	61	PG0			



SFE	Titlemega_pro_mini-3.3U-v12
SFE	Version:
	9/21/2011 2:37:12 PM
	Drawn By:
	1/1