



1/5" UXGA CMOS Image Sensor
GC2035

DataSheet
V1.0

2012-09-18

GalaxyCore Inc.

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P1:0xb1	LSC_Q3_green_b1	8	0x20	RW	LSC Q3 green b1
P1:0xb2	LSC_Q3_blue_b1	8	0x20	RW	LSC Q3 blue b1
P1:0xb3	LSC_Q4_red_b1_0	8	0x20	RW	LSC Q4 red b10
P1:0xb4	LSC_Q4_green_b1	8	0x20	RW	LSC Q4 green b1
P1:0xb5	LSC_Q4_blue_b1	8	0x20	RW	LSC Q4 blue b1
P1:0xb6	LSC_right_red_b2_0	8	0x20	RW	LSC right red b20
P1:0xb7	LSC_right_green_b2	8	0x20	RW	LSC right green b2
P1:0xb8	LSC_right_blue_b2	8	0x20	RW	LSC right blue b2
P1:0xb9	LSC_right_red_b4_0	8	0x20	RW	LSC right red b40
P1:0xba	LSC_right_green_b4	8	0x20	RW	LSC right green b4
P1:0xbb	LSC_right_blue_b4	8	0x20	RW	LSC right blue b4
P1:0xbc	LSC_left_red_b2_0	8	0x20	RW	LSC left red b20
P1:0xbd	LSC_left_green_b2	8	0x20	RW	LSC left green b2
P1:0xbe	LSC_left_blue_b2	8	0x20	RW	LSC left blue b2
P1:0xbf	LSC_left_red_b4_0	8	0x20	RW	LSC left red b40
P1:0xc0	LSC_left_green_b4	8	0x20	RW	LSC left green b4
P1:0xc1	LSC_left_blue_b4	8	0x20	RW	LSC left blue b4
P1:0xc2	LSC_up_red_b2_0	8	0x20	RW	LSC up red b20
P1:0xc3	LSC_up_green_b2	8	0x20	RW	LSC up green b2
P1:0xc4	LSC_up_blue_b2	8	0x20	RW	LSC up blue b2

P1:0xc5	LSC_up_red_b4_0	8	0x20	RW	LSC up red b40
P1:0xc6	LSC_up_green_b4	8	0x20	RW	LSC up green b4
P1:0xc7	LSC_up_blue_b4	8	0x20	RW	LSC up blue b4
P1:0xc8	LSC_down_red_b2_0	8	0x20	RW	LSC down red b20
P1:0xc9	LSC_down_green_b2	8	0x20	RW	LSC down green b2
P1:0xca	LSC_down_blue_b2	8	0x20	RW	LSC down blue b2
P1:0xcb	LSC_down_red_b4_0	8	0x20	RW	LSC down red b40
P1:0xcc	LSC_down_green_b4	8	0x20	RW	LSC down green b4
P1:0xcd	LSC_down_blue_b4	8	0x20	RW	LSC down blue b4
P1:0xd0	LSC_right_up_red_b22_0	8	0x20	RW	LSC right up red b220
P1:0xd1	LSC_right_up_red_b22_1	8	0x20	RW	LSC right up red b221
P1:0xd2	LSC_right_up_green_b22_0	8	0x20	RW	LSC right up green b220
P1:0xd3	LSC_right_up_blue_b22_0	8	0x20	RW	LSC right up blue b220
P1:0xd4	LSC_right_down_red_b22_0	8	0x20	RW	LSC right down red b220
P1:0xd5	LSC_right_down_red_b22_1	8	0x20	RW	LSC right down red b221
P1:0xd6	LSC_right_down_green_b22_0	8	0x20	RW	LSC right down green b220
P1:0xd7	LSC_right_down_blue_b22_0	8	0x20	RW	LSC right down Blue b220
P1:0xd8	LSC_left_up_red_b22_0	8	0x20	RW	LSC left up red b220
P1:0xd9	LSC_left_up_red_b22_1	8	0x20	RW	LSC left up red b221
P1:0xda	LSC_left_up_green_b22_0	8	0x20	RW	LSC left up green b22_0
P1:0xdb	LSC_left_up_blue_b22_0	8	0x20	RW	LSC left up blue b220

	e_b22_0				
P1:0xdc	LSC_left_down_red_b22_0	8	0x20	RW	LSC left down red b220
P1:0xdd	LSC_left_down_red_b22_1	8	0x20	RW	LSC left down red b221
P1:0xde	LSC_left_down_green_b22_0	8	0x20	RW	LSC left down green b220
P1:0xdf	LSC_left_down_blue_b22_0	8	0x20	RW	LSC left down blue b220
P1:0xe0	LSC_Q1_red_b1_1	8	0x20	RW	LSC Q1 red b11
P1:0xe1	LSC_Q2_red_b1_1	8	0x20	RW	LSC Q2 red b11
P1:0xe2	LSC_Q3_red_b1_1	8	0x20	RW	LSC Q3 red b11
P1:0xe3	LSC_Q4_red_b1_1	8	0x20	RW	LSC Q4 red b11
P1:0xe4	LSC_right_red_b2_1	8	0x20	RW	LSC right red b21
P1:0xe5	LSC_right_red_b4_1	8	0x20	RW	LSC right red b41
P1:0xe6	LSC_left_red_b2_1	8	0x20	RW	LSC left red b21
P1:0xe7	LSC_left_red_b4_1	8	0x20	RW	LSC left red b41
P1:0xe8	LSC_up_red_b2_1	8	0x20	RW	LSC up red b21
P1:0xe9	LSC_up_red_b4_1	8	0x20	RW	LSC up red b41
P1:0xea	LSC_down_red_b2_1	8	0x20	RW	LSC down red b21
P1:0xeb	LSC_down_red_b4_1	8	0x20	RW	LSC down red b41
P1:0x4a	LSC_right_green_b4_1	8	0x20	RW	A LSC right green b41
P1:0x4b	LSC_left_green_b4_1	8	0x20	RW	A LSC left green b41
P1:0x4c	LSC_up_green_b4_1	8	0x20	RW	A LSC up green b41
P1:0x4d	LSC_down_green_b4_1	8	0x20	RW	A LSC down green b41

P1:0x4e	LSC_right_up_green_b22_1	8	0x20	RW	A LSC right up green b221
P1:0x4f	LSC_right_down_green_b22_1	8	0x20	RW	A LSC right down green b221
P1:0xce	LSC_left_up_green_b22_1	8	0x20	RW	A LSC left up green b221
P1:0xcf	LSC_left_down_green_b22_1	8	0x20	RW	A LSC left down green b221

Revision History

Version1.0 2012.09.07

- Document Release

Version1.0 2012.09.18

- AVDD28 power voltage range update